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** CONTINUING DATA *****				
** FOREIGN APPLICATIONS ***** JAPAN 160692/2001 05/29/2001 ✓ <i>me</i> JAPAN 246268/2001 08/14/2001 ✓ <i>me</i>				
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>me</i> Examiner's Signature Initials		STATE OR COUNTRY JAPAN ✓	SHEETS DRAWING 9 ✓	TOTAL CLAIMS 7 2
INDEPENDENT CLAIMS 12				
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TITLE Method for modeling semiconductor device process <i>me</i>				
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